

# Adam Magnusson

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## EDUCATION

### CHALMERS UNIVERSITY OF TECHNOLOGY

**MASTER IN HIGH PERFORMANCE COMPUTER SYSTEMS**

**BACHELOR IN COMPUTER SCIENCE**

## LINKS

Github:

[github.com/KokoRobinn](https://github.com/KokoRobinn)

LinkedIn:

[linkedin.com/in/adam-magnusson](https://linkedin.com/in/adam-magnusson)

## SKILLS

### PROGRAMMING

C • Java • C# • Haskell •  
Erlang • Elixir • Python •  
VHDL • GLSL • Go • CSS •  
HTML • React

### OTHER

Embedded Systems • Git •  
Hardware Design • Linux •  
Parallel Programming

## LANGUAGES

Swedish - Native  
English - Fluent

## REFERENCES

References can be provided at request

## ME

Driven by the prospect of personal growth, I am constantly looking for new challenges and opportunities to learn new things. As a master's student at Chalmers University of Technology I developed many of the skills I would now consider core to my main interests within the subject of computer science. These interests are mainly hardware design and low level computing, especially optimizing software with respect to the hardware it will run on.

Building on the interest of optimization, I very much enjoy parallel programming which I have done in several programming languages such as C, Erlang, Elixir, Haskell, Python and CUDA. In my free time I enjoy playing games and spending time with friends and family. I also enjoy tinkering with my server where I engage in various self-hosting endeavors on Linux.

## EXPERIENCE

**CHALMERS UNIVERSITY OF TECHNOLOGY | TEACHING ASSISTANT | PART TIME**  
Aug 2022 – Dec 2023

**THE BOARD AT THE STUDENT DIVISION FOR COMPUTER SCIENCE AND ENGINEERING | CHAIRMAN**  
May 2024 - April 2025

## RESEARCH

### OPTIMIZING STREAM ENGINES FOR USE IN EFP GAS ON RADIATION HARDENED SOCS | MASTER THESIS

Spring 2025 - Chalmers University of Technology

Developed a stream engine for fast yet resource efficient data transfer to a reconfigurable hardware accelerator. Report available at [amagnusson.se/files/Msc.pdf](https://amagnusson.se/files/Msc.pdf)

### RECONFIGURABLE HARDWARE ACCELERATOR FOR MACHINE LEARNING | BACHELOR THESIS

Spring 2023 - Chalmers University of Technology

Developed a Vector Processing Unit (VPU) to enable hardware accelerated machine learning. Report available at [amagnusson.se/files/Bsc.pdf](https://amagnusson.se/files/Bsc.pdf)